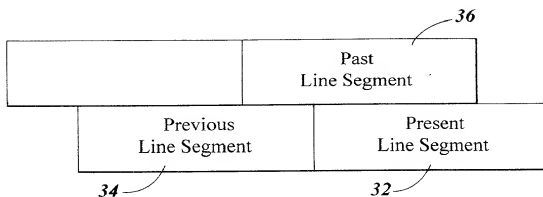
**FIG. 2****FIG. 3**

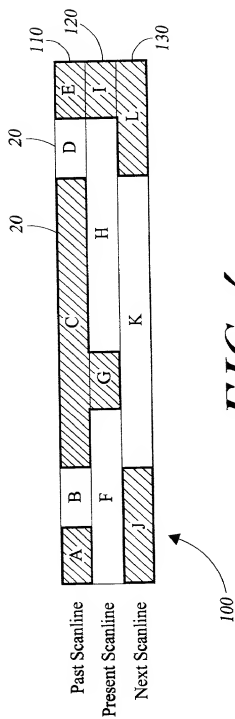
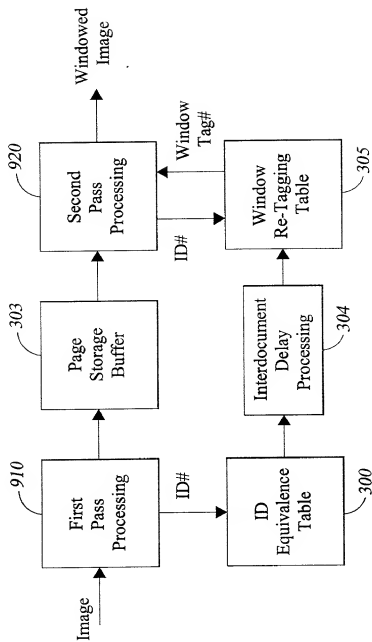
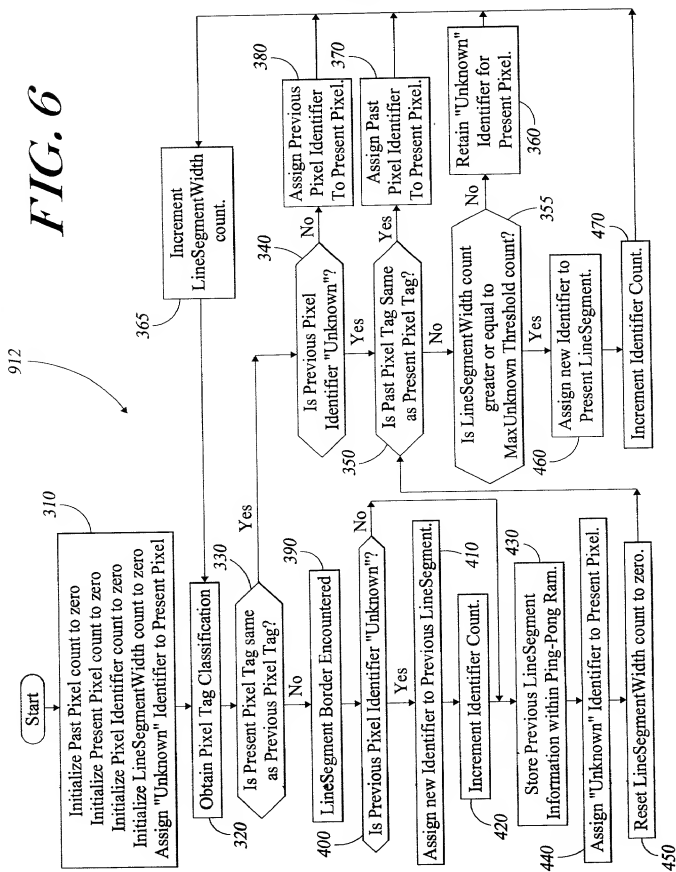


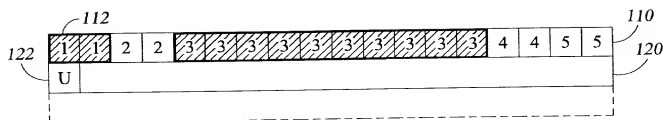
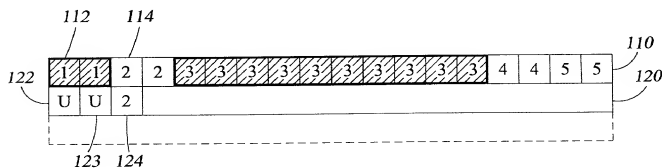
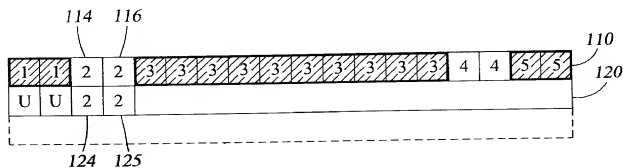
FIG. 4



**FIG. 5**

FIG. 6



**FIG. 7A****FIG. 7B****FIG. 7C**

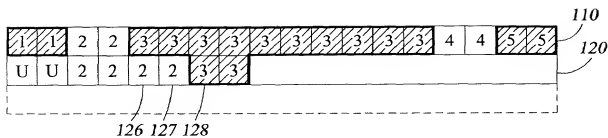
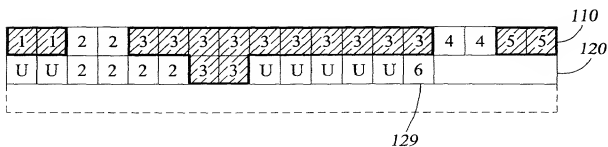
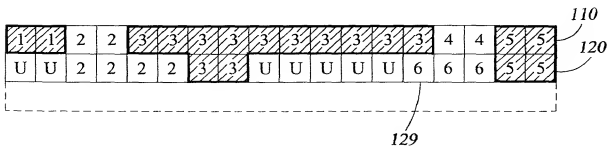
**FIG. 7D****FIG. 7E****FIG. 7F**

FIG. 8

| RAM A<br>Scanline 1 |                  |                             |    | RAM B<br>Scanline 2 |                  |                             |     |
|---------------------|------------------|-----------------------------|----|---------------------|------------------|-----------------------------|-----|
| Line Segment ID     | Line Segment Tag | Line Segment Start Position | 42 | Line Segment ID     | Line Segment Tag | Line Segment Start Position | 204 |
| 1                   | Non White        | 1                           | 43 | 2                   | White            | 1                           |     |
| 2                   | White            | 3                           |    | 3                   | NonWhite         | 7                           |     |
| 3                   | Non White        | 5                           |    | 6                   | White            | 9                           |     |
| 4                   | White            | 15                          |    | 5                   | NonWhite         | 17                          |     |
| 5                   | NonWhite         | 17                          |    | -                   | -                | -                           |     |
| -                   | -                | -                           |    | -                   | -                | -                           |     |
| -                   | -                | -                           |    | -                   | -                | -                           |     |



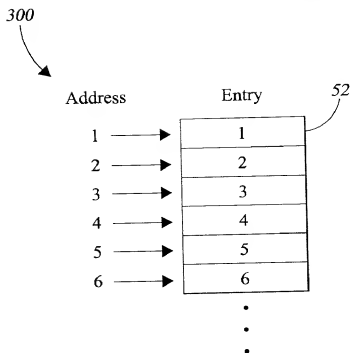
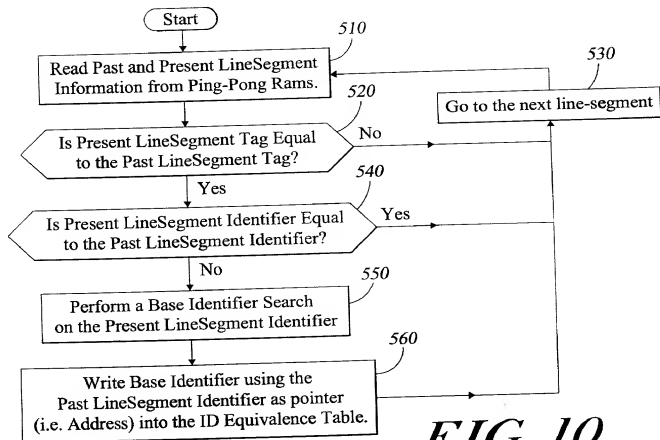
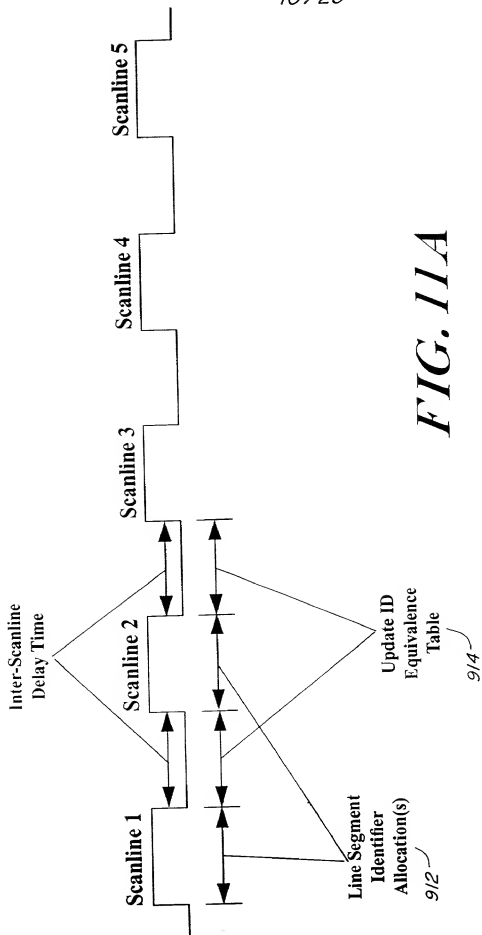


FIG. 9

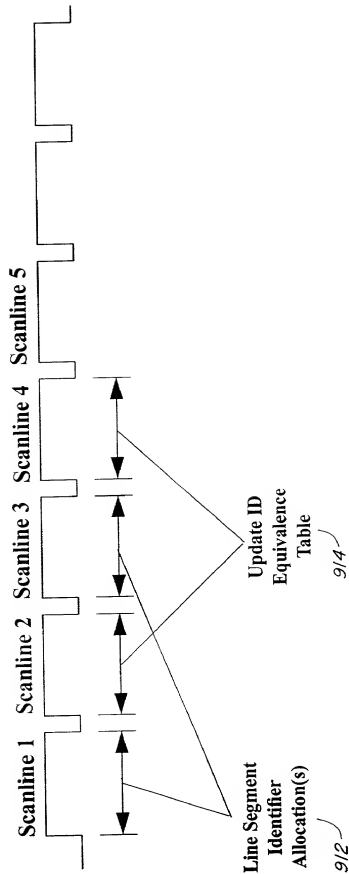


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**FIG. 11A**

**FIG. 11B**



300



| Address | Entry |
|---------|-------|
| 1       | 1     |
| 2       | 2     |
| 3       | 3     |
| 4       | 6     |
| 5       | 5     |
| 6       | 6     |
| 7       | 7     |
| -       | -     |

*FIG. 12*

|   |   |   |   |   |   |   |   |   |   |   |   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 1 | 1 | 2 | 2 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 4 | 4 | 5 | 5 |
| U | U | 2 | 2 | 2 | 2 | 3 | 3 | U | U | U | U | U | U | U | 6 | 6 | 6 | 5 | 5 |
| U | U | U | U | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | U | U | 5 | 5 |

*FIG. 13A*

|   |   |   |   |  |  |   |   |
|---|---|---|---|--|--|---|---|
| 1 | 2 | 3 |   |  |  | 4 | 5 |
| 2 |   | 3 | 6 |  |  |   | 5 |
| 7 | 2 |   |   |  |  | 5 |   |

*FIG. 13B*

200 →

RAM A  
Scanline 3

| Line Segment ID | Line Segment Tag | Line Segment Start Position |
|-----------------|------------------|-----------------------------|
| 202 →           | Non White        | 1                           |
| 206 →           | White            | 5                           |
| 210 →           | Non White        | 15                          |
| -               | -                | -                           |
| -               | -                | -                           |
| -               | -                | -                           |
| -               | -                | -                           |

RAM B  
Scanline 2

| Line Segment ID | Line Segment Tag | Line Segment Start Position |
|-----------------|------------------|-----------------------------|
| 2               | White            | 1 ← 204                     |
| 3               | Non White        | 7 ← 208                     |
| 6               | White            | 9 ← 212                     |
| 5               | Non White        | 17 ← 216                    |
| -               | -                | -                           |
| -               | -                | -                           |
| -               | -                | -                           |

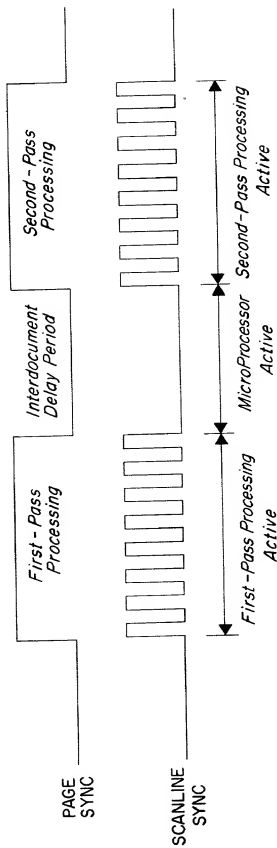
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FIG. 14

| Address | Entry |
|---------|-------|
| 1       | 1     |
| 2       | 2     |
| 3       | 3     |
| 4       | 6     |
| 5       | 5     |
| 6       | 2     |
| 7       | 7     |
| -       | -     |

300 →

***FIG. 15***



**FIG. 16**



```
graph TD
    Start([Start]) --> Init[A=1  
W=1]
    Init --> Cond620{Is A Greater Than  
Maximum Address Used?}
    Cond620 -- Yes --> Cond630{Is Entry at Address A  
Equal to A?}
    Cond620 -- No --> MarkFalse[Mark False  
In BaseID]
    MarkFalse --> SetA[Set A=A+1]
    SetA --> Cond620
    Cond630 -- Yes --> MarkTrue[Mark True  
In BaseID]
    MarkTrue --> AssignW[Assign W to Window Label  
Set W=W+1]
    AssignW --> SetConnect[Set Connect_ID=True  
Loop_Cnt=0]
    SetConnect --> Cond690{Is Connect_ID  
Equal to True?}
    Cond690 -- No --> End([End])
    Cond690 -- Yes --> SetAInc[Set A=1, BreakCnt=0]
    SetAInc --> Cond710{Is A Greater Than  
Maximum Address Used?}
    Cond710 -- Yes --> SetLoopCnt[Set LoopCnt=LoopCnt+1]
    SetLoopCnt --> Cond730{Is BaseID at  
A False?}
    Cond730 -- No --> SetLoopCnt
    Cond730 -- Yes --> SetEntry[Set Entry at A to Entry(E)  
BreakCnt=BreakCnt+1]
    SetEntry --> Cond760{Is BaseID at E True}
    Cond760 -- No --> SetAInc
    Cond760 -- Yes --> SetBaseID[Set BaseID at A to True and Set  
Window Label at A to Window  
Label of Address E.]
    SetBaseID --> Cond790{Is BreakCnt equal to Zero  
OR LoopCnt exceeds 100?}
    Cond790 -- No --> SetConnectFalse[Set Connect_ID  
to False]
    SetConnectFalse --> End
    Cond790 -- Yes --> SetAInc
```

101001-0914686

302 →

| Address | Entry <sup>307</sup> | BaseID <sup>308</sup> | Window <sup>309</sup> |
|---------|----------------------|-----------------------|-----------------------|
| 1       | 1                    | True                  | W1                    |
| 2       | 2                    | True                  | W2                    |
| 3       | 3                    | True                  | W3                    |
| 4       | 6                    | False                 |                       |
| 5       | 5                    | True                  | W4                    |
| 6       | 2                    | False                 |                       |
| 7       | 7                    | True                  | W5                    |
| -       | -                    | -                     | -                     |

**FIG. 18A**

302 →

| Address | Entry <sup>307</sup> | BaseID <sup>308</sup> | Window <sup>309</sup> |
|---------|----------------------|-----------------------|-----------------------|
| 1       | 1                    | True                  | W1                    |
| 2       | 2                    | True                  | W2                    |
| 3       | 3                    | True                  | W3                    |
| 4       | 2                    | False                 |                       |
| 5       | 5                    | True                  | W4                    |
| 6       | 2                    | True                  | W2                    |
| 7       | 7                    | True                  | W5                    |
| -       | -                    | -                     | -                     |

**FIG. 18B**

| Address | Entry <sup>307</sup> | BaseID <sup>308</sup> | Window <sup>309</sup> |
|---------|----------------------|-----------------------|-----------------------|
| 1       | 1                    | True                  | W1                    |
| 2       | 2                    | True                  | W2                    |
| 3       | 3                    | True                  | W3                    |
| 4       | 2                    | True                  | W2                    |
| 5       | 5                    | True                  | W4                    |
| 6       | 2                    | True                  | W2                    |
| 7       | 7                    | True                  | W5                    |
| -       | -                    | -                     | -                     |

302 →

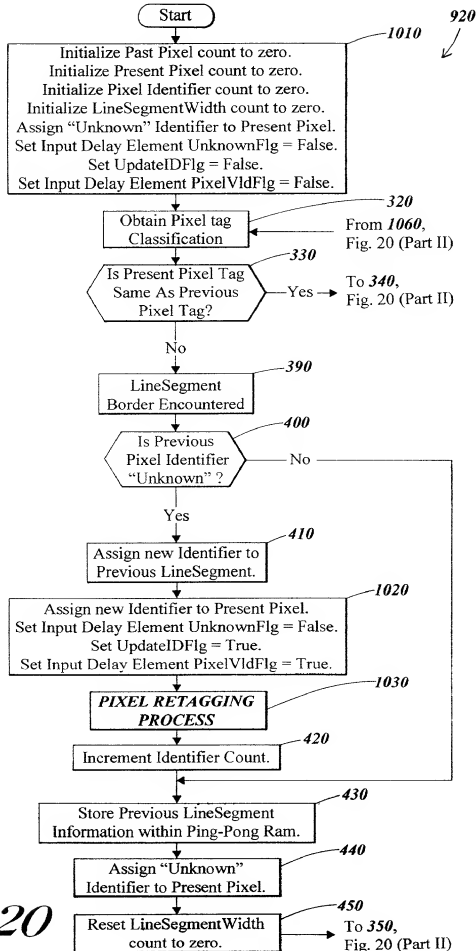
**FIG. 18C**

| Address | Window Label |
|---------|--------------|
| 1       | W1           |
| 2       | W2           |
| 3       | W3           |
| 4       | W2           |
| 5       | W4           |
| 6       | W2           |
| 7       | W5           |
| -       | -            |

309

305

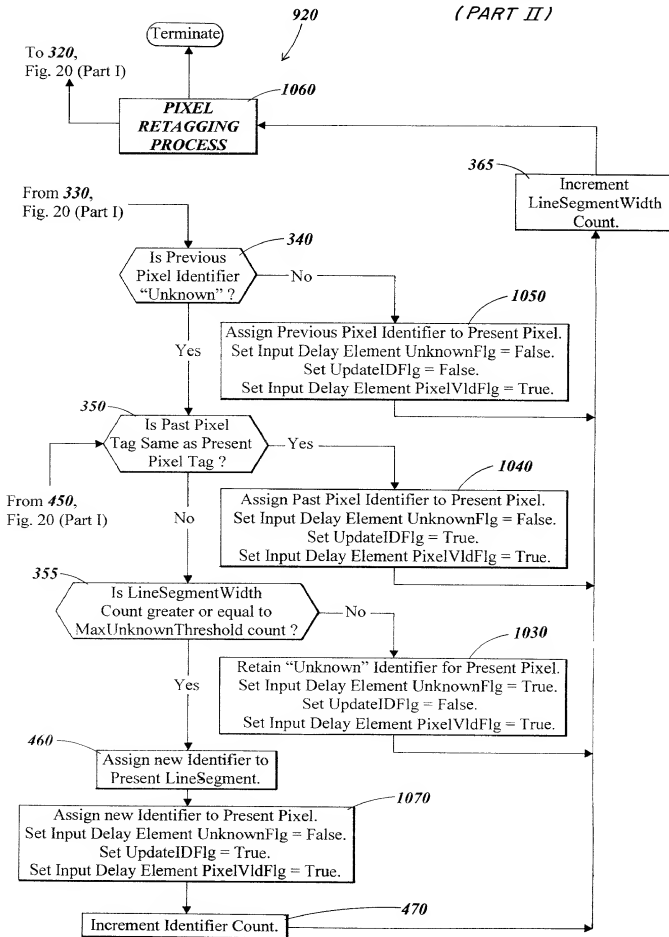
**FIG. 19**

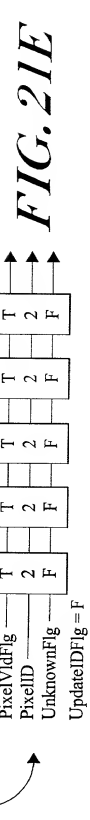
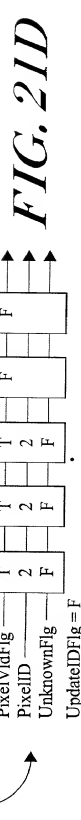
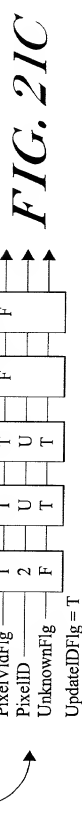
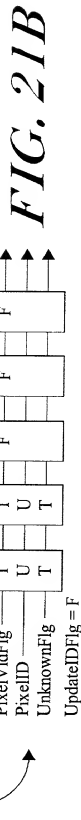
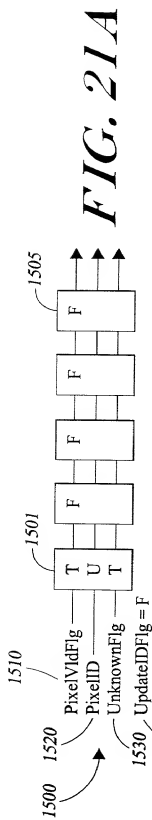


**FIG. 20**  
(PART I)

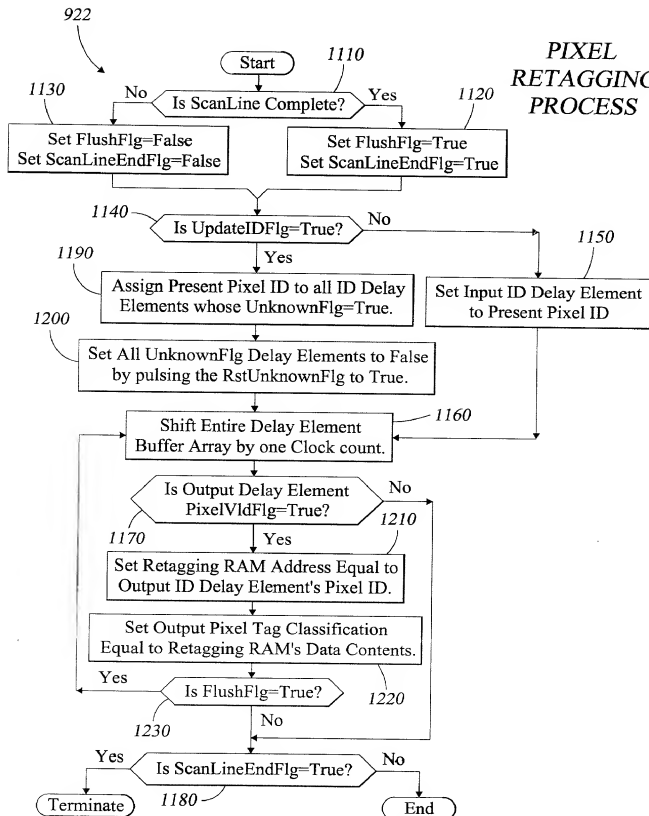
# FIG. 20

(PART II)



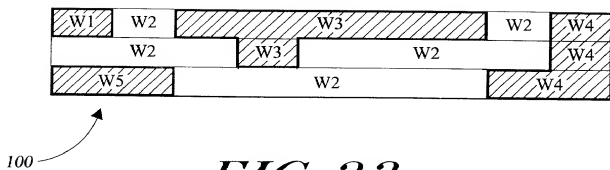


# PIXEL RETAGGING PROCESS

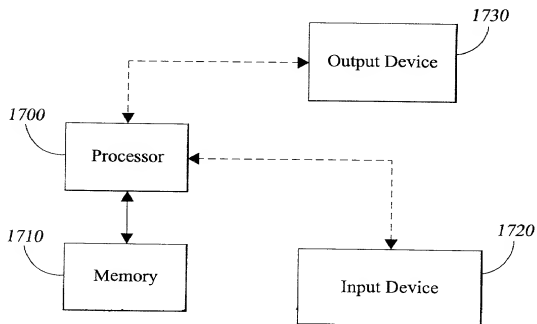


## FIG. 22





*FIG. 23*



**FIG. 24**